

Gokaraju Rangaraju Institute of Engineering and Technology

Department of Computer Science and Engineering

PATENTS

S.No.	Title of the Patent	File Number	Date	Names of the Patenter	Status		
2019-20							
	Method for design of current differencing buffered amplifier using computing device	201911023802 A	05/07/2019	Dr.G.R.Sakthidharan	2019 Published		

- 12) PATENT APPLICATION PUBLICATION
- 19) INDIA
- 22) Date of filing of Application :15/06/2019
- (21) Application No.201911023802 A
- (43) Publication Date: 05/07/2019

54) Title of the invention : METHOD FOR DESIGN OF CURRENT DIFFERENCING BUFFERED AMPLIFIER USING COMPUTING DEVICE

(51) International classification (31) Priority Document No (32) Priority Date (33) Name of priority country (86) International Application No Filing Date	:G01R31/006 :NA :NA :NA :NA :NA	(71)Name of Applicant: 1)Mr. Udit Mamodiya Address of Applicant: Department of Electrical Engineering Assistant Professor Poornima Institute of Engineering & Technology, Jaipur, Rajasthan, India Rajasthan India 2)Dr. Piyush Kumar Shukla 3)Dr. G. R. Sakthidharan 4)Dr. Prashant Kumar Shukla 5)Dr. Rashmi Soni 6)Dr. Deepika Chauhan
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The present invention refers to method for design of current differencing buffered amplifier using computing device. It disclose a method of design, analysis and modify the electronic circuit using the model with a computing device and pre-stored algorithm in processing unit of the computing device, with providing specific facilitation in displaying analyzed information in display unit of the computing device. The method provides facility to the user to perform required changes the circuit parameters with the changing the outcomes display of the information.

No. of Pages: 19 No. of Claims: 7