

HPC & Parallel Programming Approaches



ASCI RED (1970) Super computer

9297 Intel Pro processors, 2.38 Tera Floating Point operations per sec



❑ 2017 Intel Xeon Phi processor

With same power in single chip

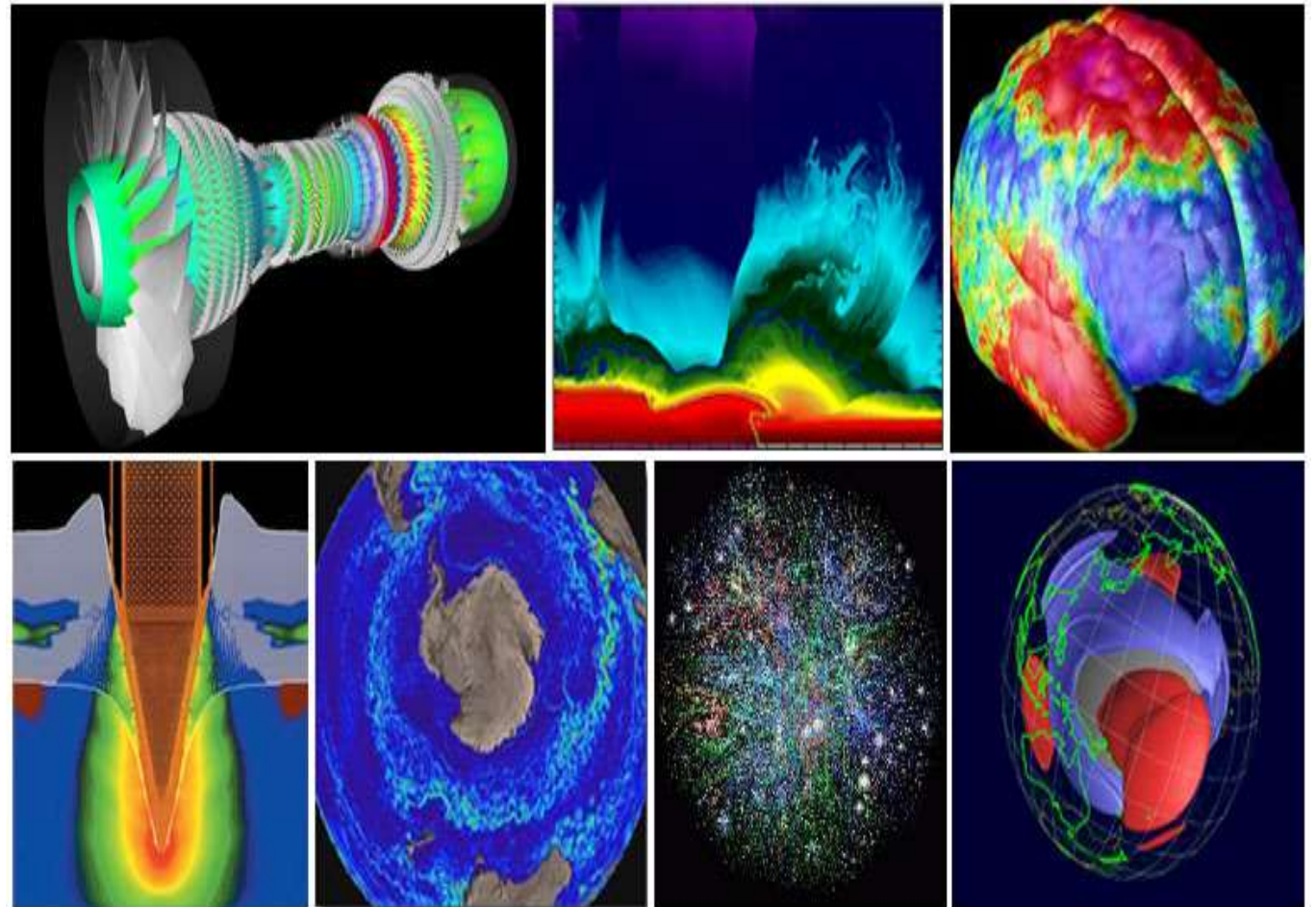
❑ **CORI** (9152 Intel Xeon Phi processors with 140000 Tera Floating Point operations per sec)

Installation

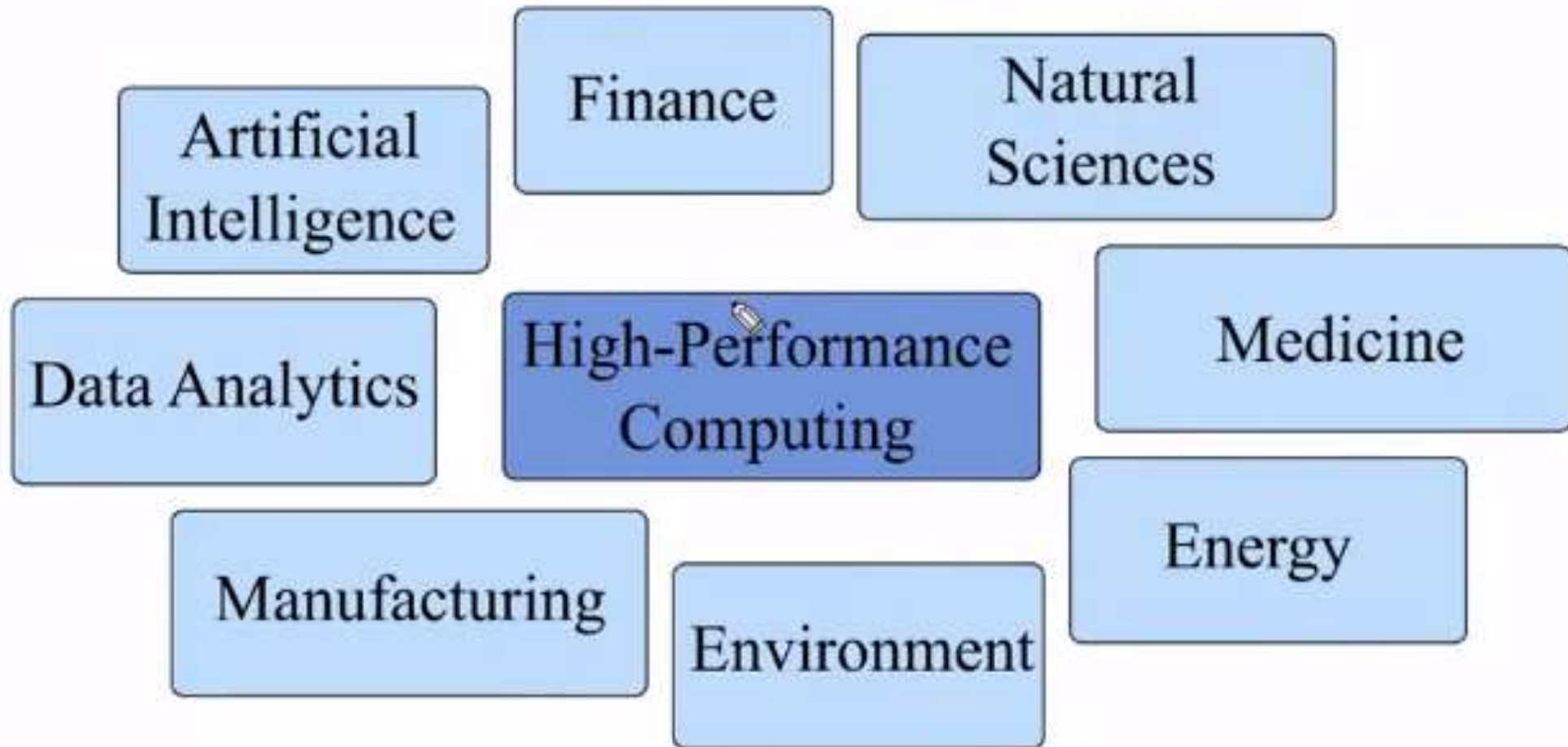
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High performance computing enable exciting Science

- ❑ Finding cures for diseases (radiation therapy) and predicting natural disasters (earthquake prediction)
- ❑ Development of clean energy (wind turbines) and protection of environment (climate modelling)
- ❑ Automobiles safer (Virtual crash tests) and teach computers to detect crime (fraud detection)
- ❑ Create machines to help people with disabilities (Natural Speech applications)



HPC & Parallel Programming Approaches



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Performance Needs

Shorter
time to
insight

Better
power
efficiency

Reduced
hardware
costs

Greater
problem
sizes

**HPC Aspects:
exploit
parallelism**

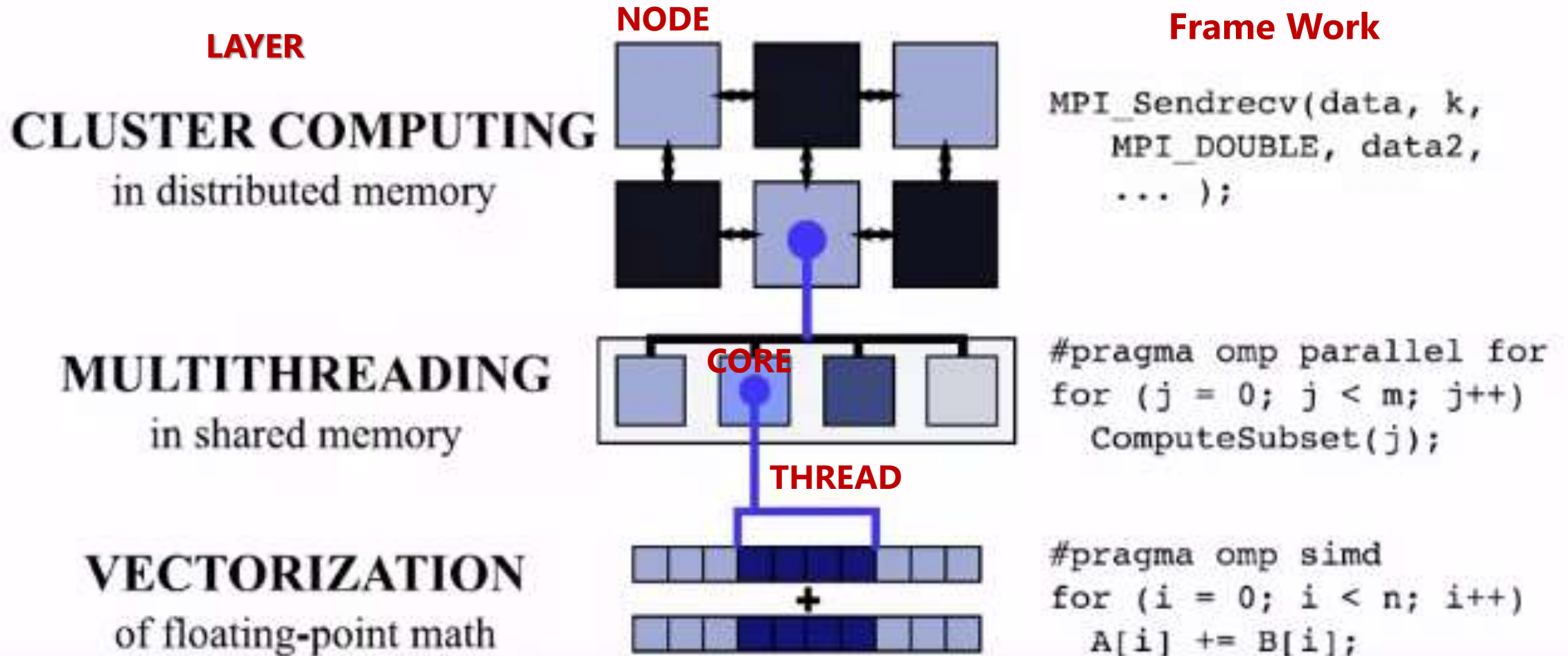
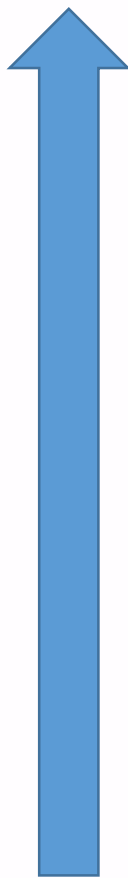
**Evolution of Hardware
Computer Architecture**

Optimized Software

Novel Platforms

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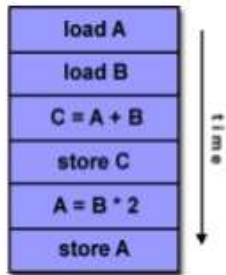
Parallel Programming layers



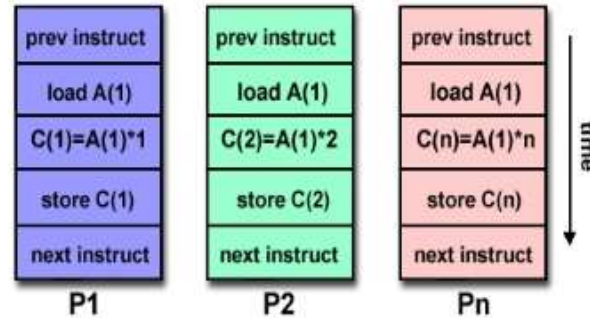
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Parallel Computing Taxonomy

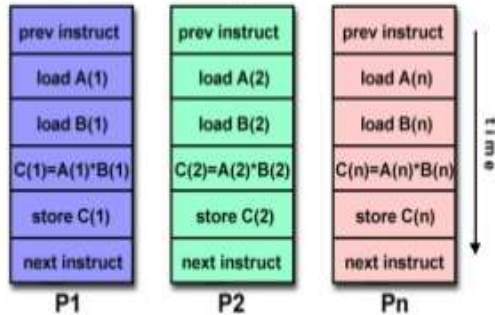
- SISD: A serial computer



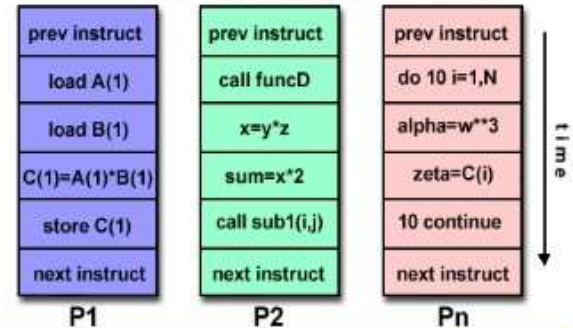
- MISD: Cryptographic Decoding



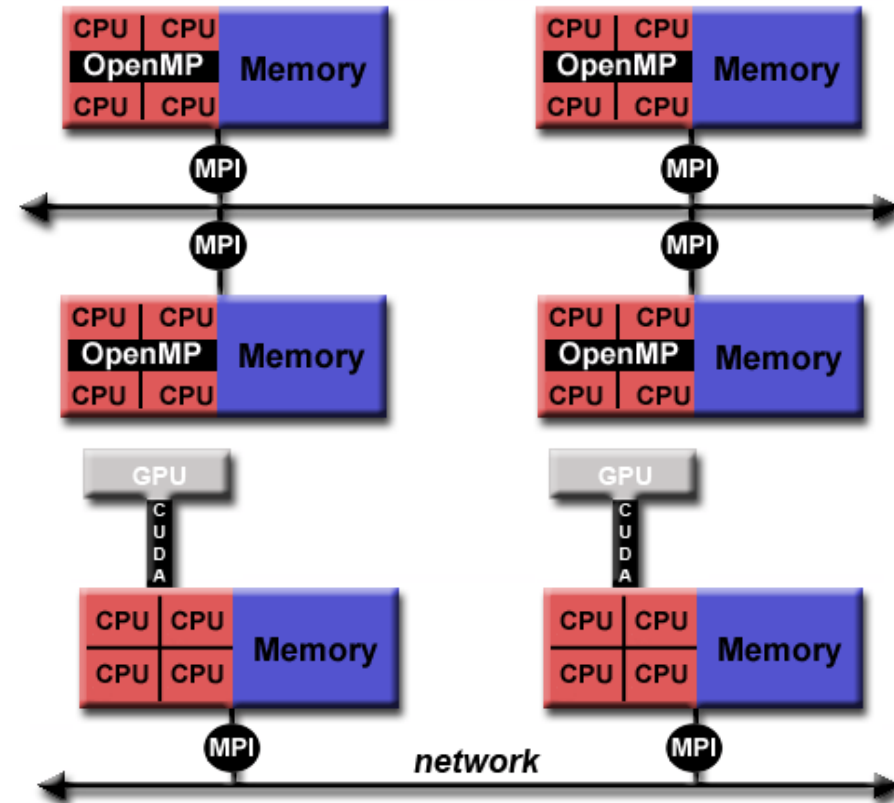
- SIMD: GPUs



- MIMD: Clusters, Supercomputers



Parallel Computing Architectures



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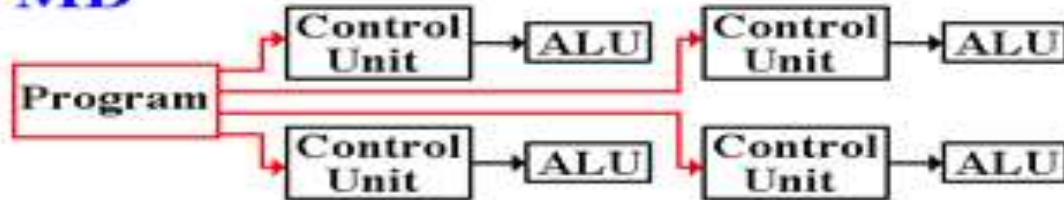
SISD



SIMD



SPMD



MIMD



Threads versus Processes

Option 1: Partitioning data set between threads/processes

